

Form PTO-499		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-1533		SERIAL NO. 09/754,926	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Kie Y. Ahn et al.		RECEIVED TECHNOLOGY CENTER NOV 26 2002	
					FILING DATE January 4, 2001			
U.S. PATENT DOCUMENTS								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
EK	AA	6,093,612	07/00	Suh	—	—		
	AB							
	AC							
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	AH							
	AI							
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	AL							
FOREIGN PATENT DOCUMENTS								
	Document Number	Date	Country	Class	Subclass	Translation		
						Yes	No	
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
EK	AR		Wolf, S., "Silicon Processing for the VLSI Era", Vol. 2: Process Integration, 1990 Lattice Press, Sunset Beach, CA,					
			pp. 332-333.					
	AS							
	AT							
EXAMINER				DATE CONSIDERED				
Erik Kie				2/1/03				
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								